

## TESSERA LED PROCESSORS - 2023 Comparison Table

This document is intended to provide an overview of major features and differences between Brompton Technology LED processors. For more detailed information please visit [www.bromptontech.com](http://www.bromptontech.com) or contact [support@bromptontech.com](mailto:support@bromptontech.com)

| VIDEO INPUTS  | SX40   | S8   | S4  | T1  |
|---|--|--|---|---|
| SDI Inputs<br>DVI/HDMI Inputs<br>HDR Support<br>Maximum supported Frame Rate  | 1 X 12G-SDI<br>1 X HDMI 2.0<br>PQ-HDR10, HLG<br>HDMI: 250Hz (Maximum pixel clock of 600MHz)<br>SDI: 60Hz | 1 X 12G-SDI<br>1 X HDMI 2.0<br>PQ-HDR10, HLG<br>HDMI: 250Hz (Maximum pixel clock of 600MHz)<br>SDI: 60Hz | None<br>1 X DVI<br>None<br>60Hz                   | None<br>1 X DVI<br>None<br>60Hz           |
| <b>VIDEO OUTPUTS</b>  |  |  |   |   |
| Output Ports<br>Closed Loop Redundancy<br>Processor Redundancy  | 4 x 10G Ethernet (copper or fibre to XD)<br>Yes<br>Yes   | 8 x 1G Ethernet (copper)<br>Yes<br>No  | 4 x 1G Ethernet (copper)<br>Yes<br>No             | 1 x 1G Ethernet (copper)<br>No<br>No      |
| <b>CAPACITY</b>   |  |  |   |   |
| Nominal Pixel Capacity<br>Maximum Canvas Size<br>Non-Standard Canvas Size Support<br>Maximum Number Of Panels/Fixtures            | 9 Million<br>4096X2160<br>Yes<br>2000  | 4.5 Million<br>4096X2160<br>Yes<br>2000  | 2.1 Million<br>1920X1080<br>Yes<br>2000           | 0.5 Million<br>1920X1080<br>Yes<br>500    |
| <b>VIDEO PROCESSING</b>   |  |  |   |   |
| Region Of Interest (ROI)<br>Output Scaling  | No<br>Yes  | No<br>Yes  | No<br>No  | Yes<br>Yes                                |
| <b>SOFTWARE FEATURES</b>  |  |  |   |   |
| ChromaTune<br>Panel Rotation<br>Supports Sub-fixtures from 2.2.6<br>Frame Re-Mapping<br>ShutterSync<br>Tracking Marker Support    | Yes<br>90°/180°/270° only<br>Yes<br>Yes<br>Yes<br>Yes  | Yes<br>90°/180°/270° only<br>Yes<br>Yes<br>Yes<br>Yes  | No<br>90°/180°/270° only<br>Yes<br>No<br>No<br>No | No<br>Any Degree<br>Yes<br>No<br>No<br>No |
| <b>GENLOCK/LATENCY</b>  |  |  |   |   |
| Genlock to Video Inputs<br>End-to-end Latency With All Features<br>End-to-end Latency In Low Latency Mode<br>Genlock Phase Offset | Yes<br>2 Frames<br>1 Frame<br>Yes  | Yes<br>2 Frames<br>1 Frame<br>Yes  | Yes<br>2 Frames<br>N/A<br>No                      | Yes<br>3 Frames<br>2 Frames<br>Yes        |
| <b>CONTROL</b>  |  |  |   |   |
| DMX-512 /Art-Net /sACN Live Control<br>IP Control   | Yes<br>Yes   | Yes<br>Yes   | Yes<br>Yes  | Yes<br>Yes                                |